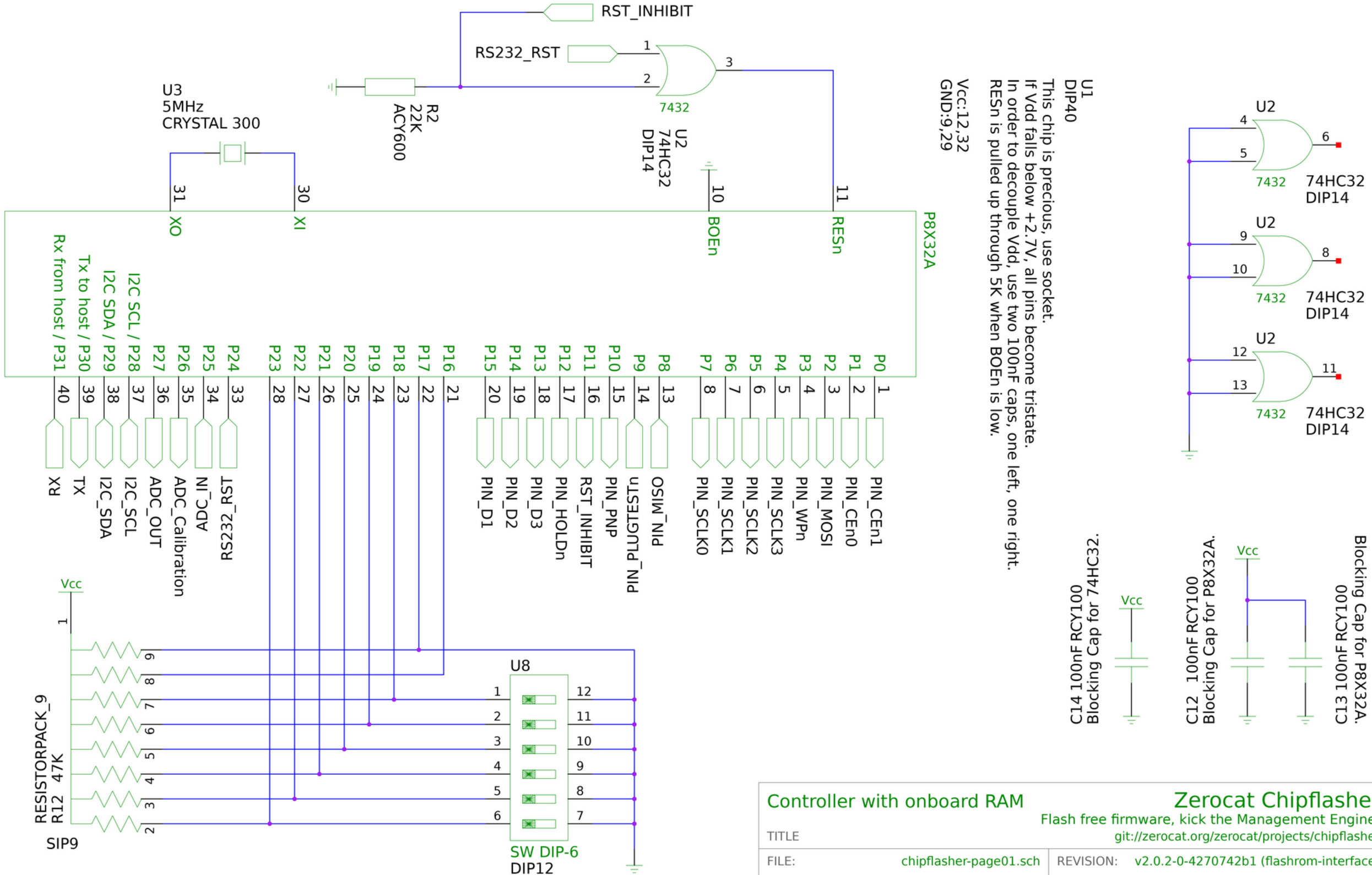
RST_INHIBIT
RS232_RST
R2
22K
ACY600
7432
U2
74HC32
DIP14
U3
5MHz
CRYSTAL 300
U1
DIP40
This chip is precious, use socket.
If Vdd falls below +2.7V, all pins become tristate.
In order to decouple Vdd, use two 100nF caps, one left, one right.
RESn is pulled up through 5K when BOEn is low.
Vcc:12,32
GND:9,29
P8X32A
XO
XI
BOEn
RESn
RX
TX
I2C_SDA
I2C_SCL
ADC_OUT
ADC_Calibration
IN_ADV
RS232_RST
PIN_D1
PIN_D2
PIN_D3
PIN_HOLDn
RST_INHIBIT
PIN_PNP
PIN_PLUGTESTn
PIN_MISO
PIN_SCLK0
PIN_SCLK1
PIN_SCLK2
PIN_SCLK3
PIN_WPn
PIN_MOSI
PIN_CEn0
PIN_CEn1
Rx from host / P31
Tx to host / P30
I2C SDA / P29
I2C SCL / P28
Vcc
RESISTORPACK_9
R12 47K
SIP9
U8
SW DIP-6
DIP12
C14 100nF RCY100
Blocking Cap for 74HC32.
C12 100nF RCY100
Blocking Cap for P8X32A.
C13 100nF RCY100
Blocking Cap for P8X32A.
Controller with onboard RAM
Zerocat Chipflasher
Flash free firmware, kick the Management Engine.
TITLE
git://zerocat.org/zerocat/projects/chipflasher
FILE: chipflasher-page01.sch
REVISION: v2.0.2-0-4270742b1 (flashrom-interface)
PAGE OF
DRAWN BY: kmx