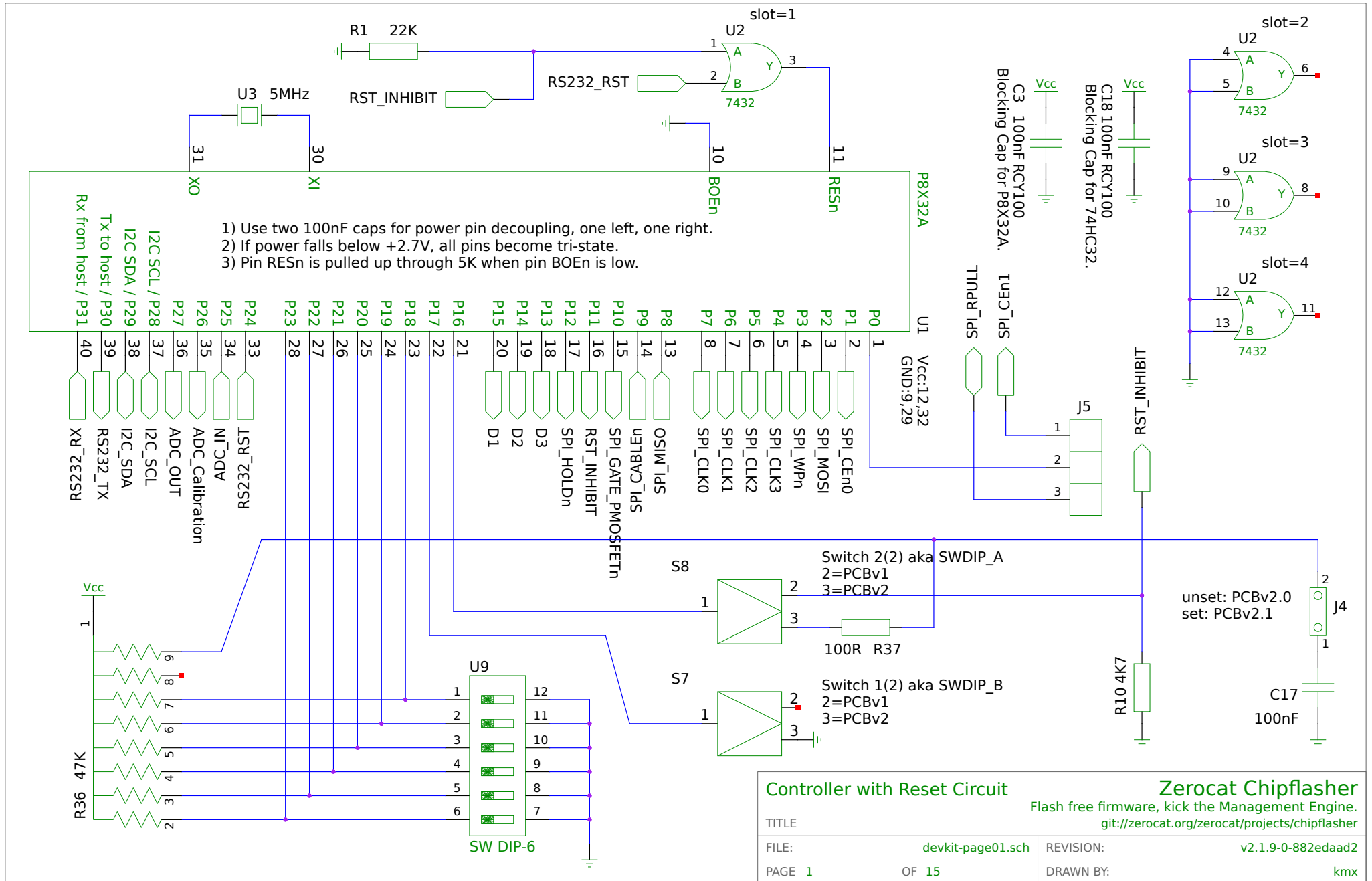
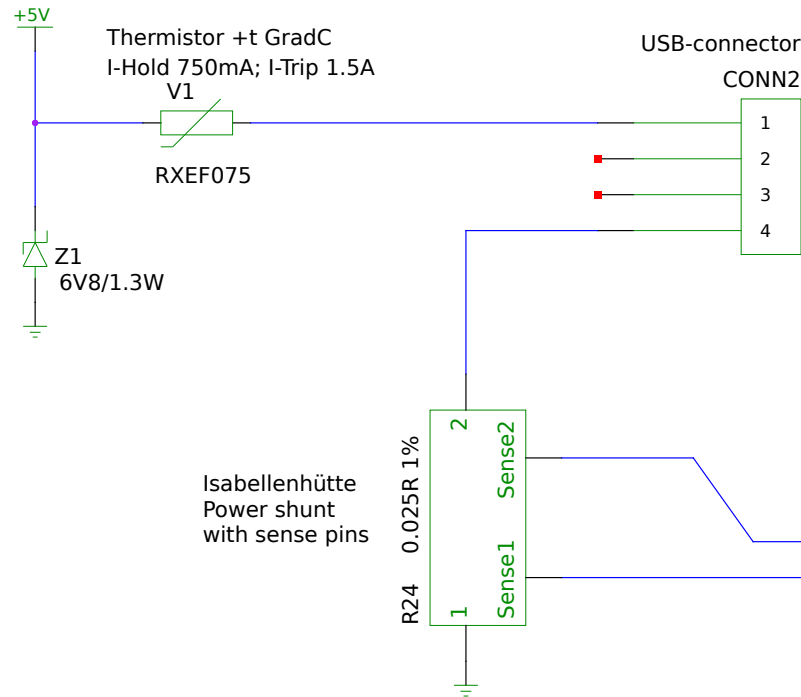






USB Power

chips: 5V@100mA,
use one USB-Port
boards: 5V@500mA,
use external Adapter

GND-Sense

Power Input with Sense

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

FILE: devkit-page02.sch

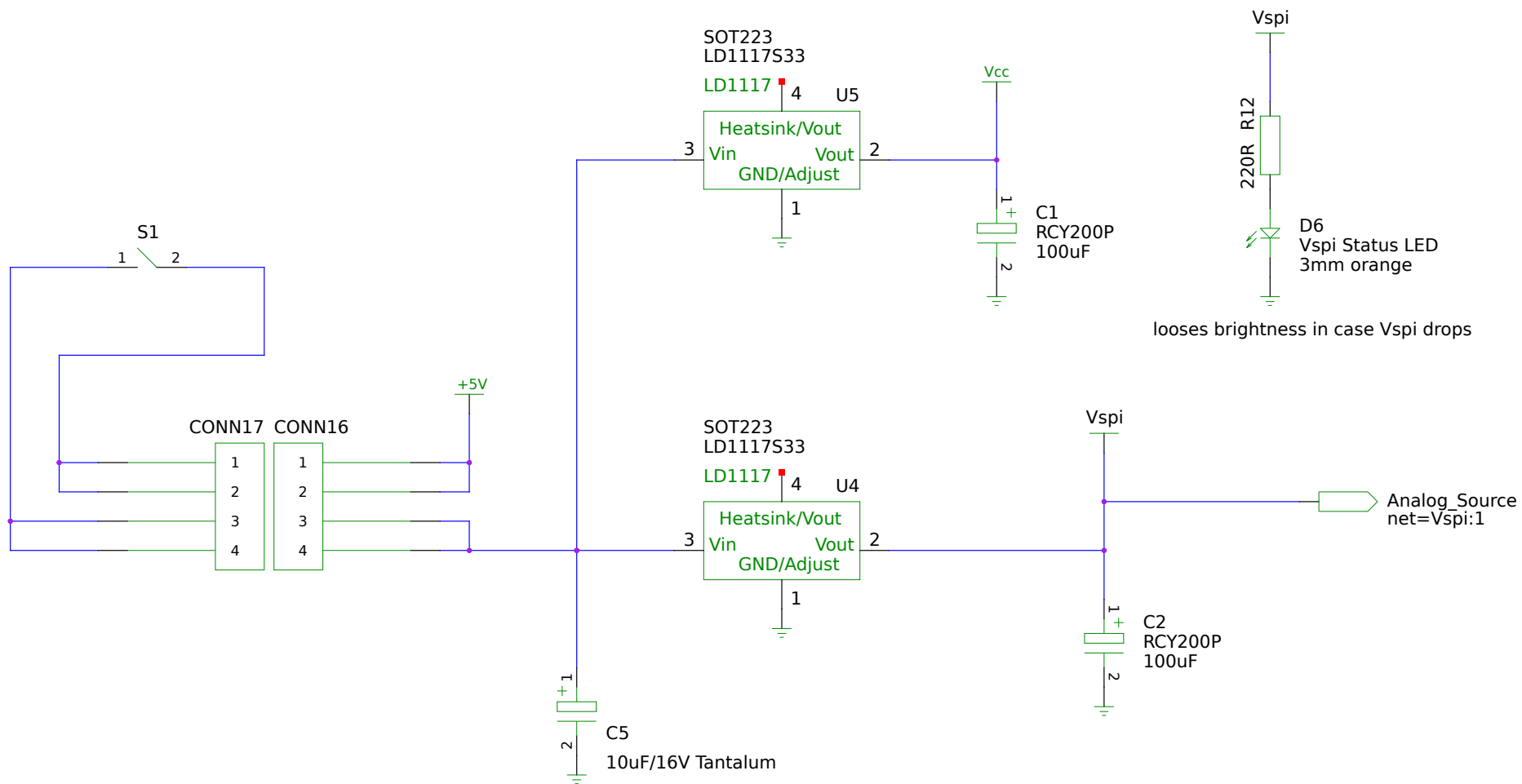
REVISION: v2.1.9-0-882edaad2

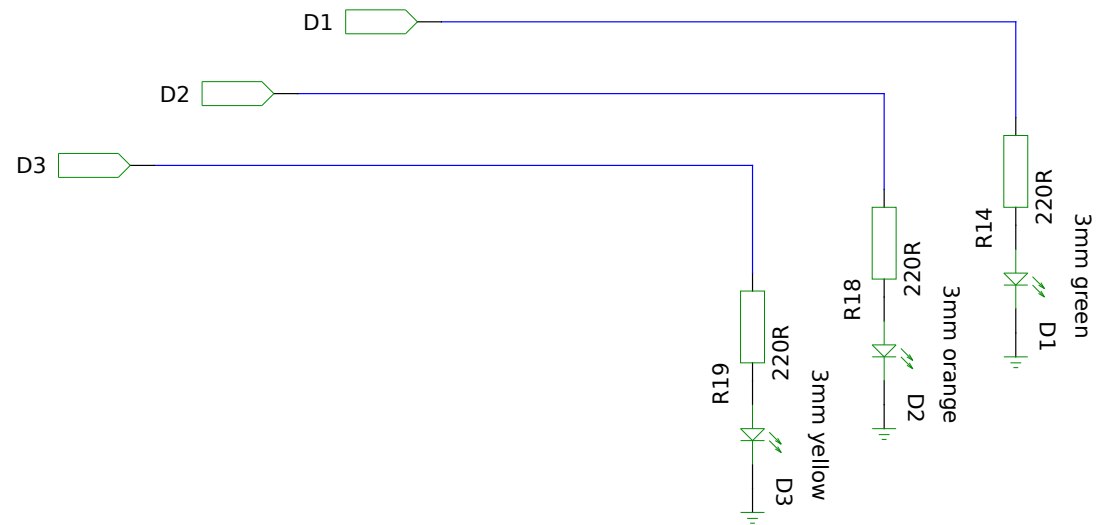
PAGE 2

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DRAWN BY:

kmx





Software Status LEDs

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

FILE:

devkit-page05.sch

REVISION:

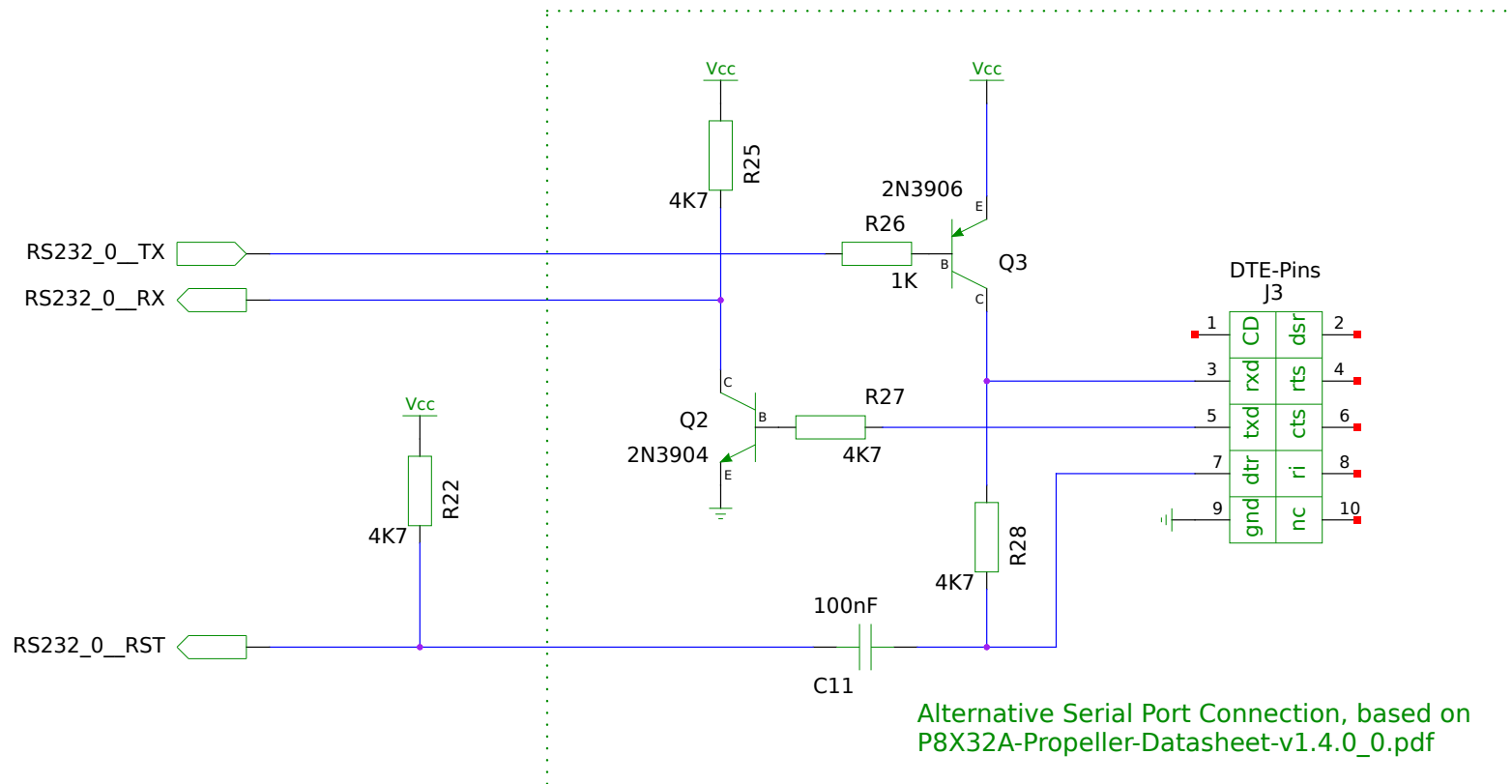
v2.1.9-0-882edaad2

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kmx

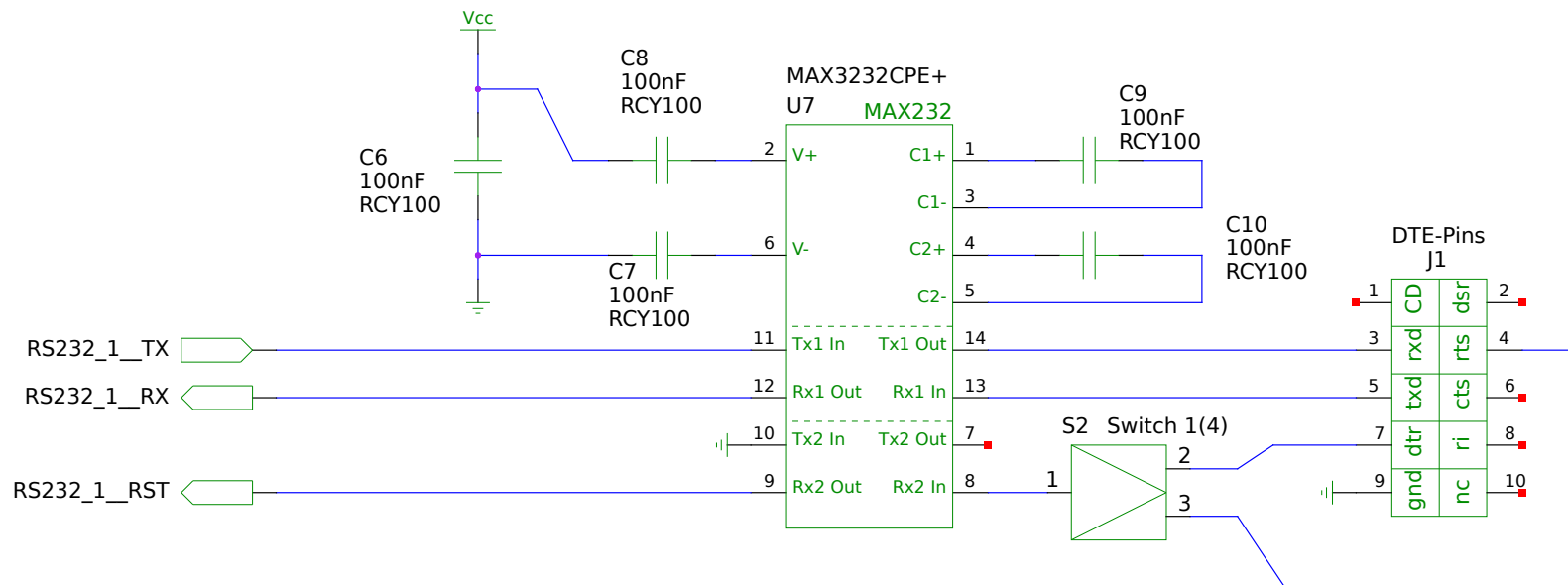


RS232 Circuit #0

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE		
FILE:	devkit-page06.sch	REVISION:
PAGE 6	OF 20	v2.1.9-0-882edaad2
		DRAWN BY:
		kmx



This circuit is generating RS232 voltages of about +/- 5.5VDC.

RS232 Circuit #1

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

FILE: devkit-page07.sch

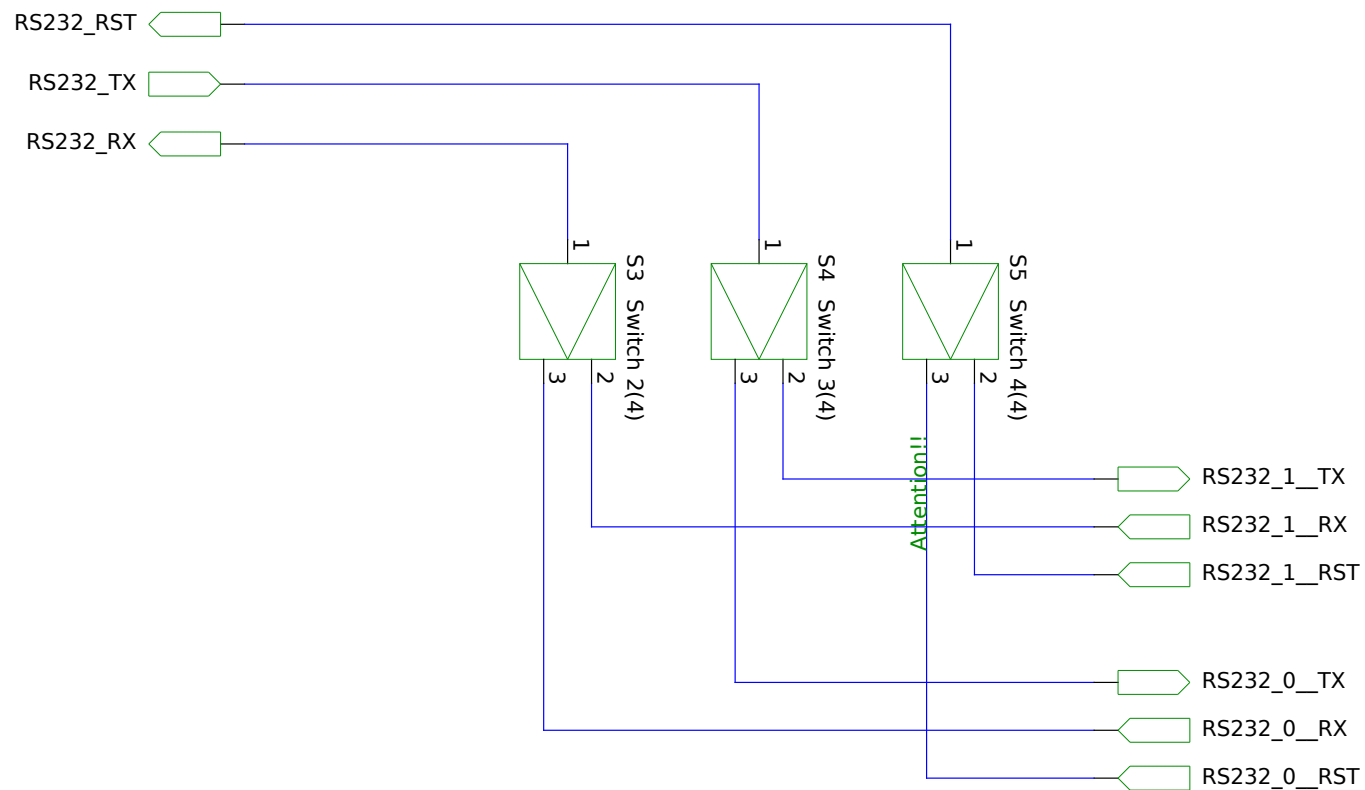
REVISION: v2.1.9-0-882edaad2

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kmx

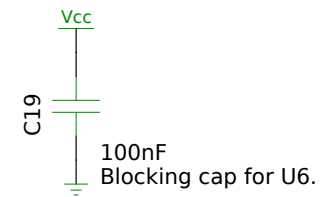
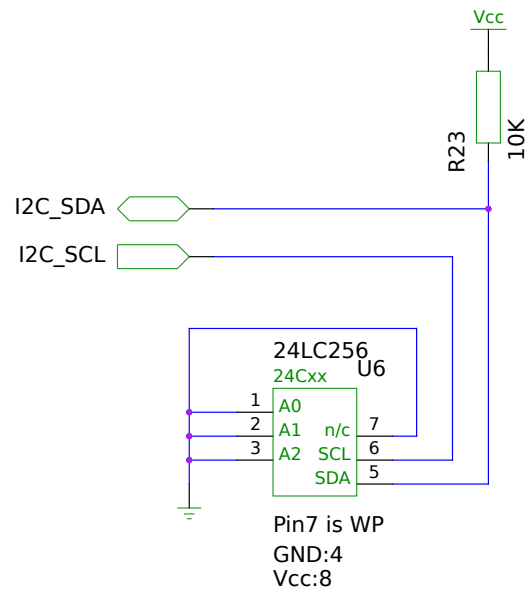


RS232 Circuit Selector

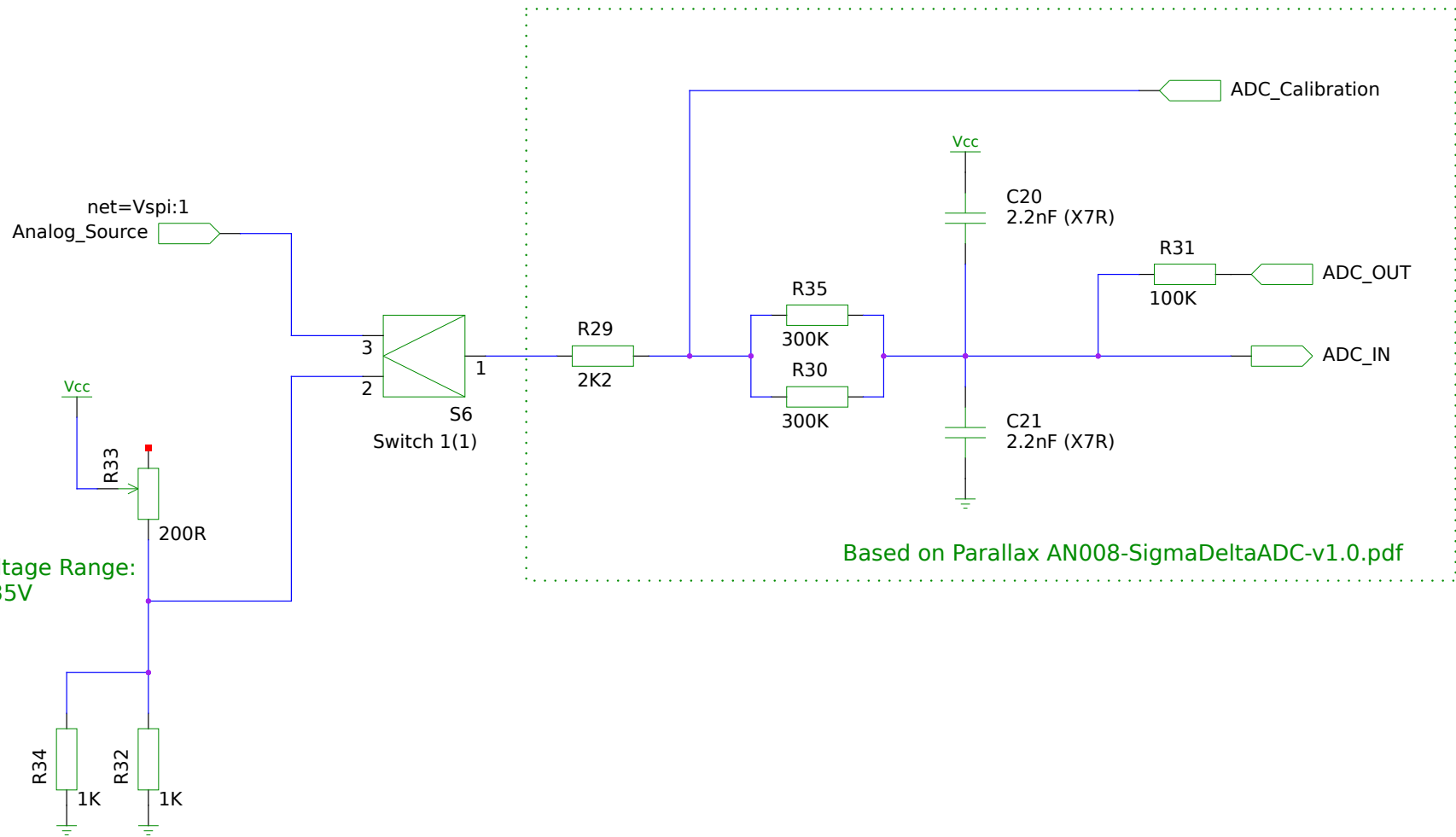
Zerocat Chipflasher

Flash free firmware, kick the Management Engine.
git://zerocat.org/zerocat/projects/chipflasher

TITLE	
FILE:	devkit-page08.sch
REVISION:	v2.1.9-0-882edaad2
PAGE 8	OF 22
DRAWN BY:	kmx



EEPROM of a non-free design, optional		ZeroCat Chipflasher	
Flash free firmware, kick the Management Engine.		git://zerocat.org/zerocat/projects/chipflasher	
FILE:	devkit-page09.sch	REVISION:	v2.1.9-0-882edaad2
PAGE 9	OF 23	DRAWN BY:	kmx



Sigma-delta ADC Circuit

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

FILE:

devkit-page10.sch

REVISION:

v2.1.9-0-882edaad2

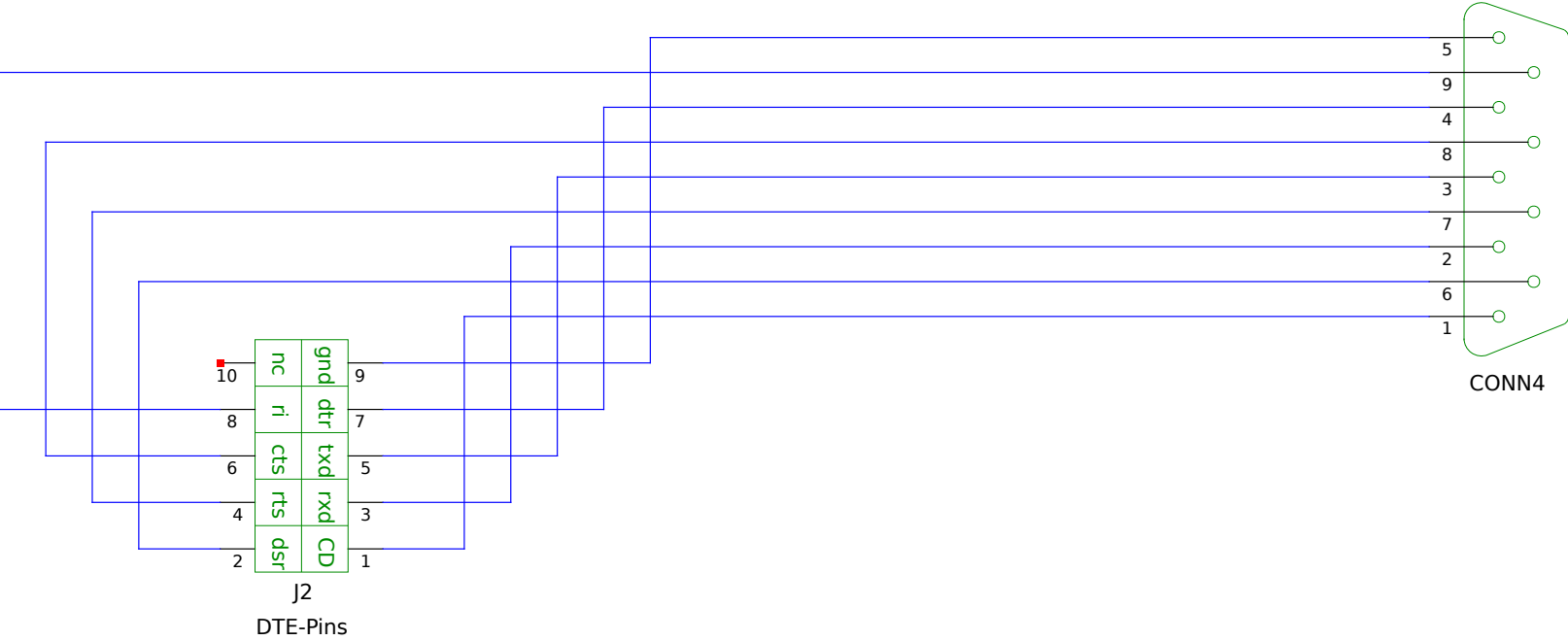
PAGE 10

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kmx

Note:
DTE = Data Terminal Equipment (Laptop)
DCE = Data Communication Equipment (Propeller)
DTR = Data Terminal Ready (Laptop is ready to send/receive)

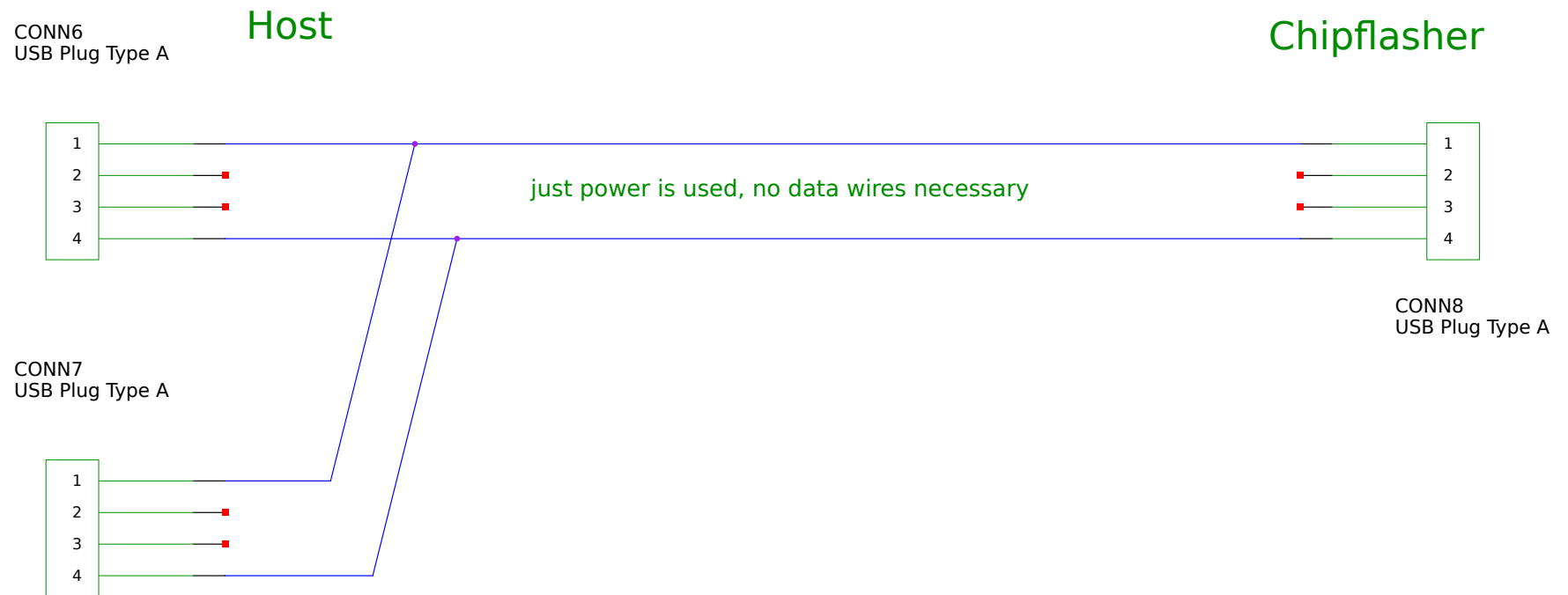


RS232 Cable

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

FILE:	devkit-page11.sch	REVISION:	v2.1.9-0-882edaad2
PAGE 11	OF 25	DRAWN BY:	kmx



Y-USB Power Cable

Zerocat Chipflasher

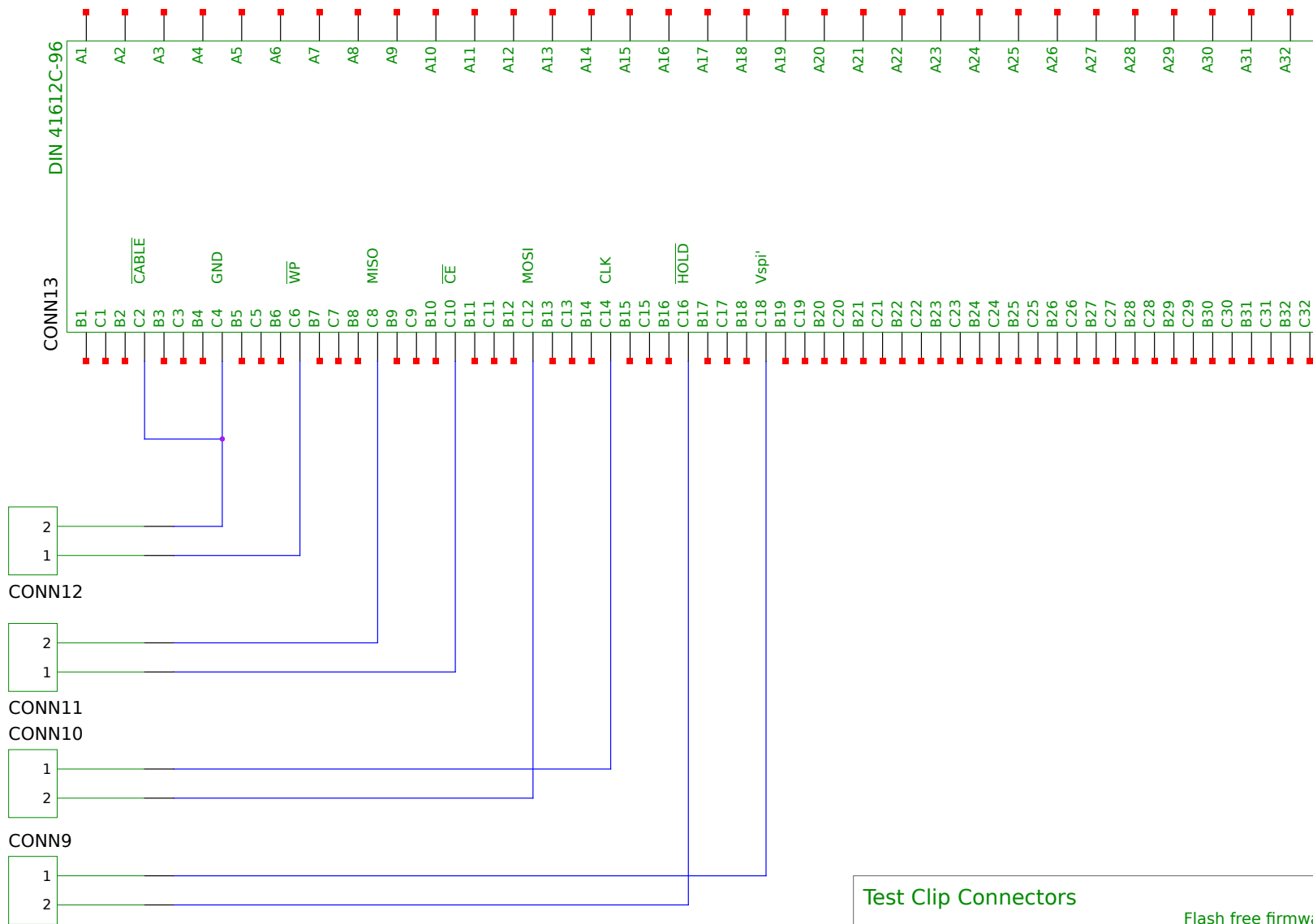
Flash free firmware, kick the Management Engine.
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

FILE: devkit-page12.sch

REVISION: v2.1.9-0-882edaad2

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DRAWN BY: kmx

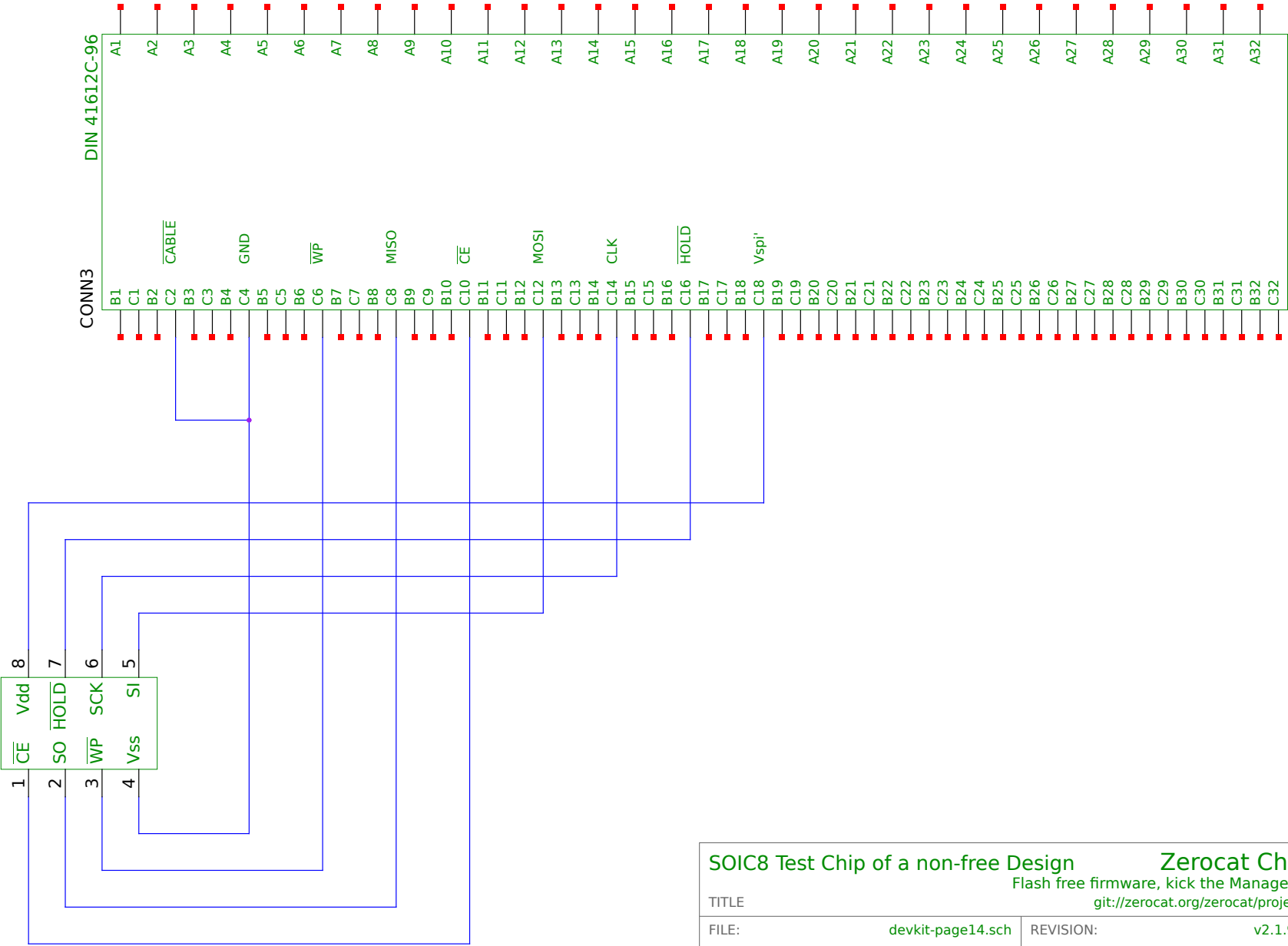


Test Clip Connectors		ZeroCAT Chipflasher	
TITLE		Flash free firmware, kick the Management Engine. git://zerocat.org/zerocat/projects/chipflasher	
FILE:	devkit-page13.sch	REVISION:	v2.1.9-0-882edaad2
PAGE 13	OF 27	DRAWN BY:	kmx

8105

Chips with same Pinout:
M95020W
MX25L1605D

SST25VF016B U8



SOIC8 Test Chip of a non-free Design

Zerocat Chipflasher

Flash free firmware, kick the Management Engine.

[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

FILE: devkit-page14.sch

REVISION: v2.1.9-0-882edaad2

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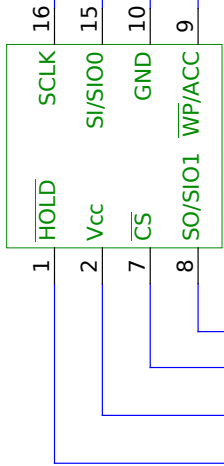
OF 28

DRAWN BY:

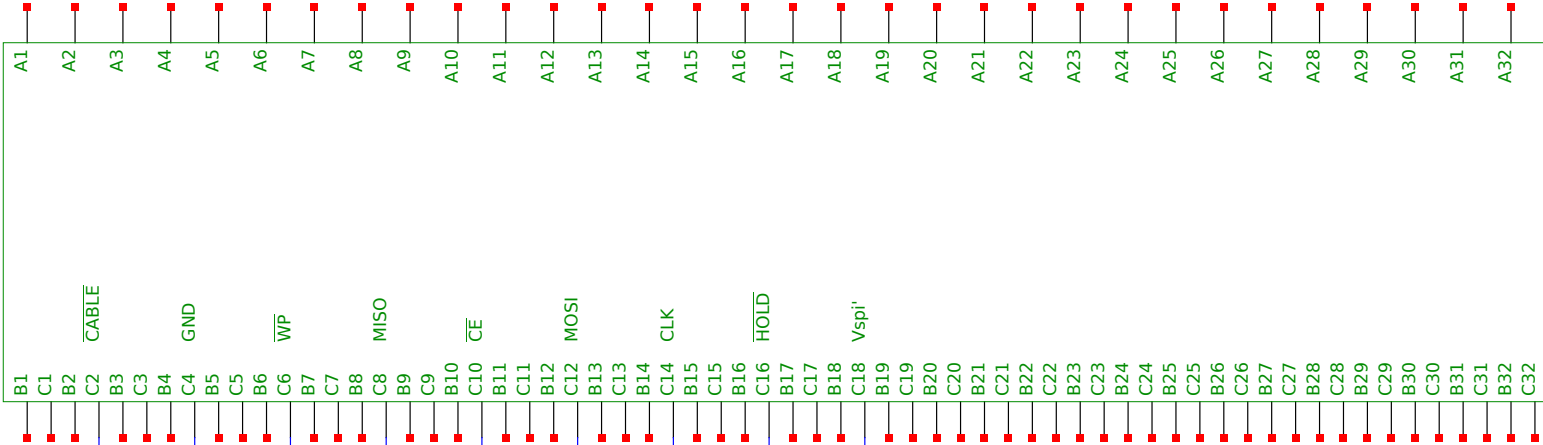
kmx

SOIC16

MX25L6405D U10



CONN5 DIN 41612C-96



SOIC16 Test Chip of a non-free Design		ZeroCat Chipflasher	
Flash free firmware, kick the Management Engine.		git://zerocat.org/zerocat/projects/chipflasher	
TITLE			
FILE:	devkit-page15.sch	REVISION:	v2.1.9-0-882edaad2
PAGE 15	OF 29	DRAWN BY:	kmx