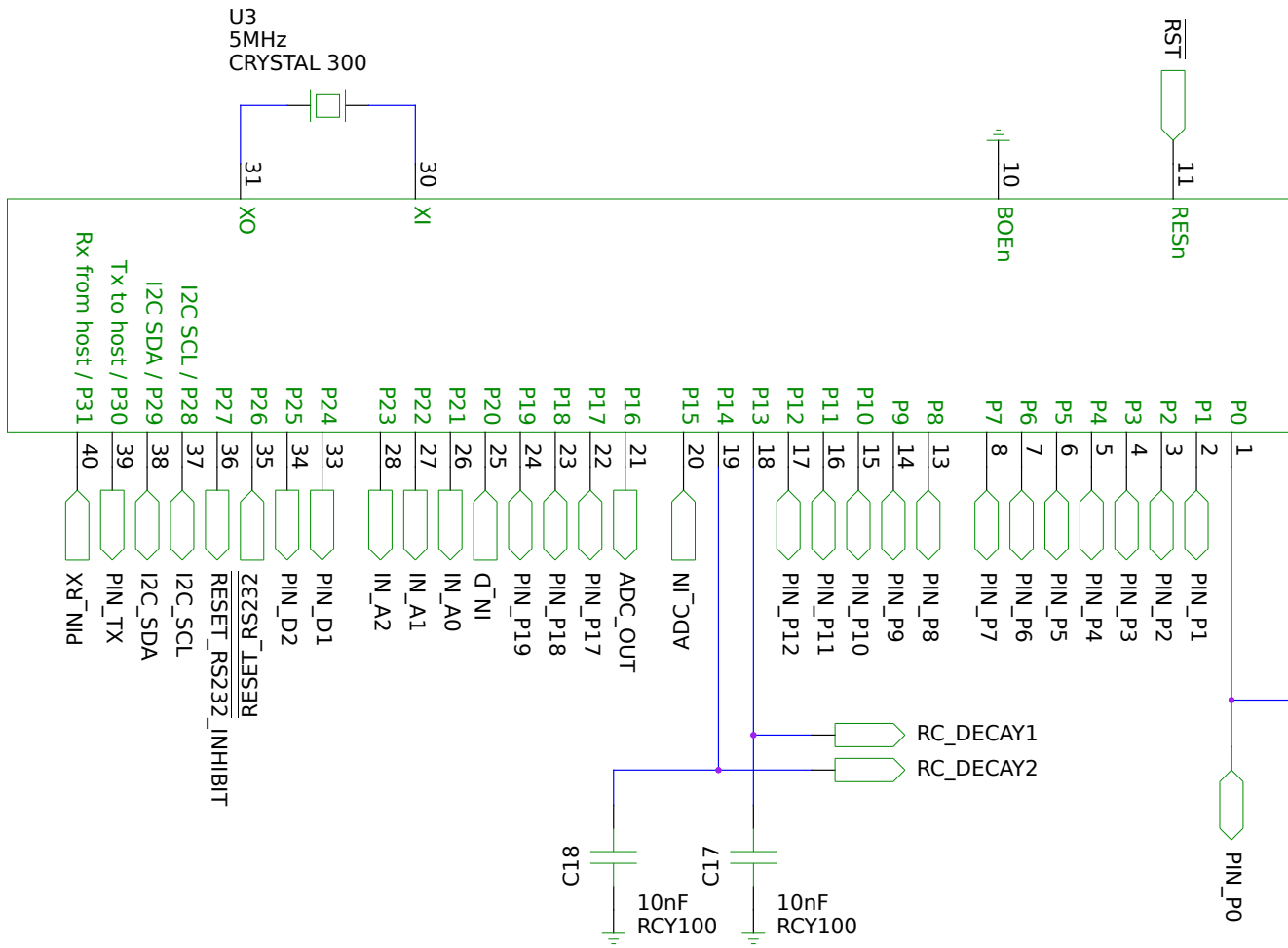


U1
DIP40

This chip is precious, use socket.
If Vdd falls below +2.7V, all pins become tristate.
RESn is pulled up through 5K when BOEn is low.

Vcc:12,32
GND:9,29

P8X32A



Controller with RAM, free design

ZeroCat Dev Kit

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TITLE

FILE:

multiboard-page01.sch

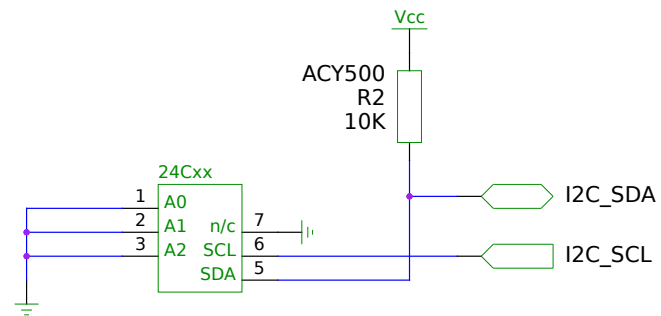
REVISION:

2117b7b4b (master)

PAGE

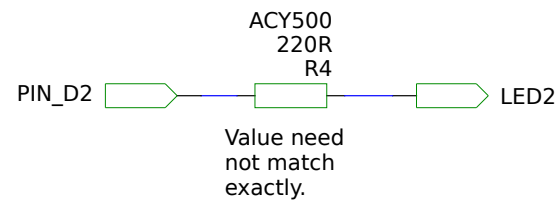
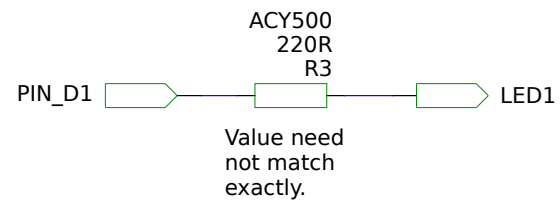
OF

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U4
24LC256
DIP8
This chip is optional. Use socket.

Serial EEPROM (optional)		Zerocat Dev Kit	
TITLE		Put the fun back into hardware development! git://zerocat.org/zerocat/projects/zerocat-dev-kit.git	
FILE:	multiboard-page02.sch	REVISION:	2117b7b4b (master)
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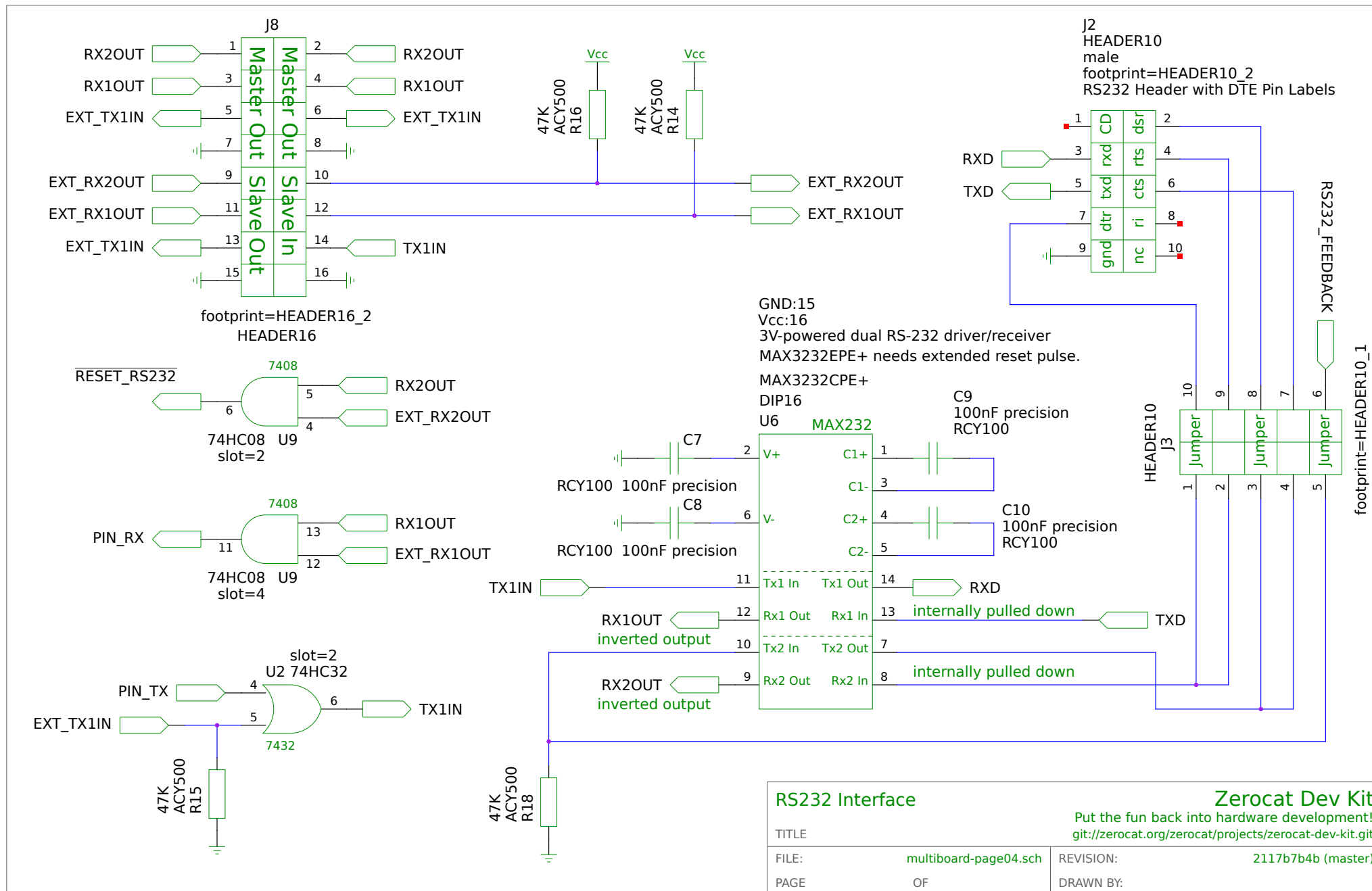


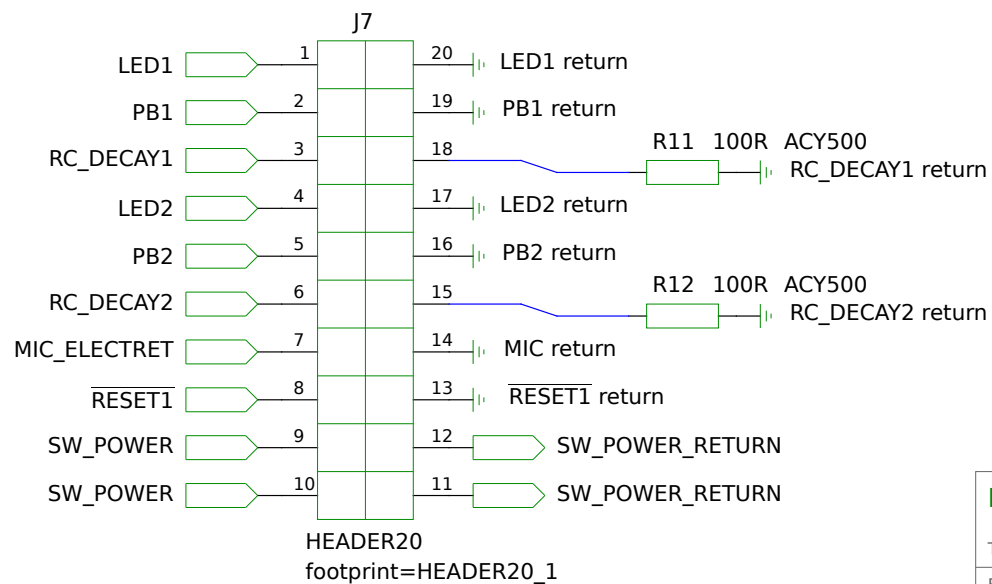
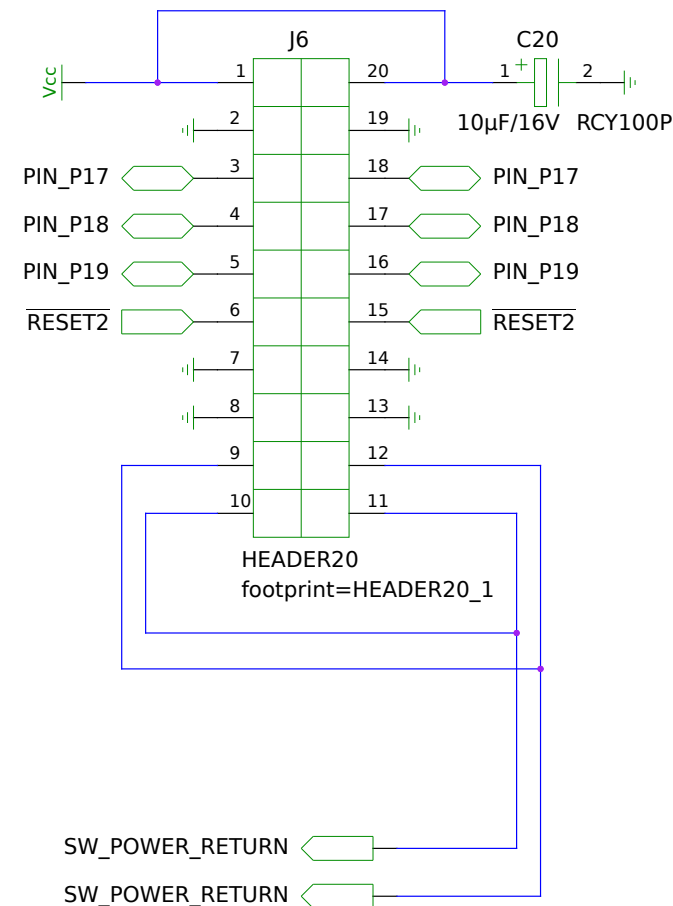
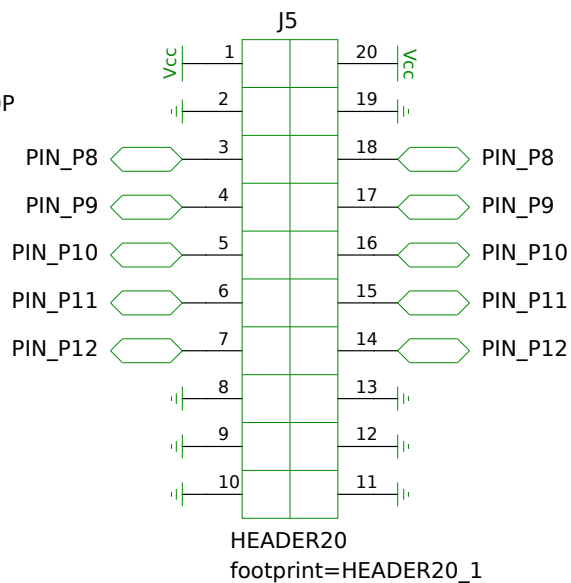
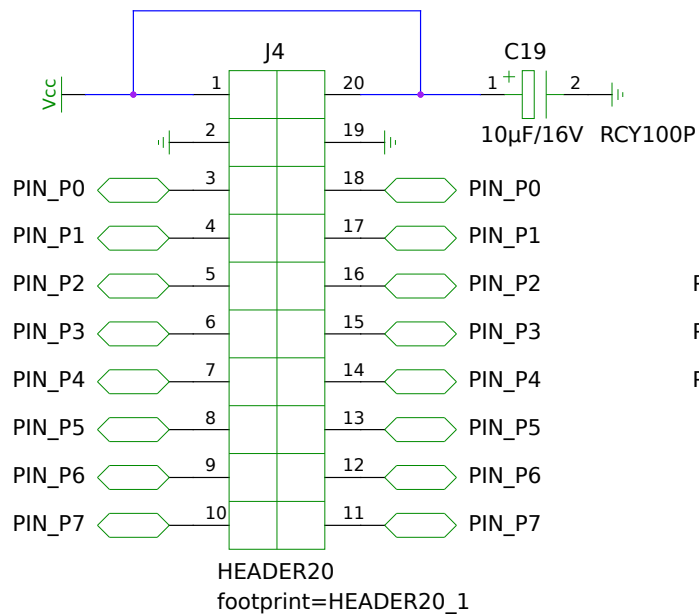
Software Status LEDs

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FILE:	multiboard-page03.sch	REVISION:	2117b7b4b (master)
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PCB Connectors

TITLE

FILE: multiboard-page05.sch

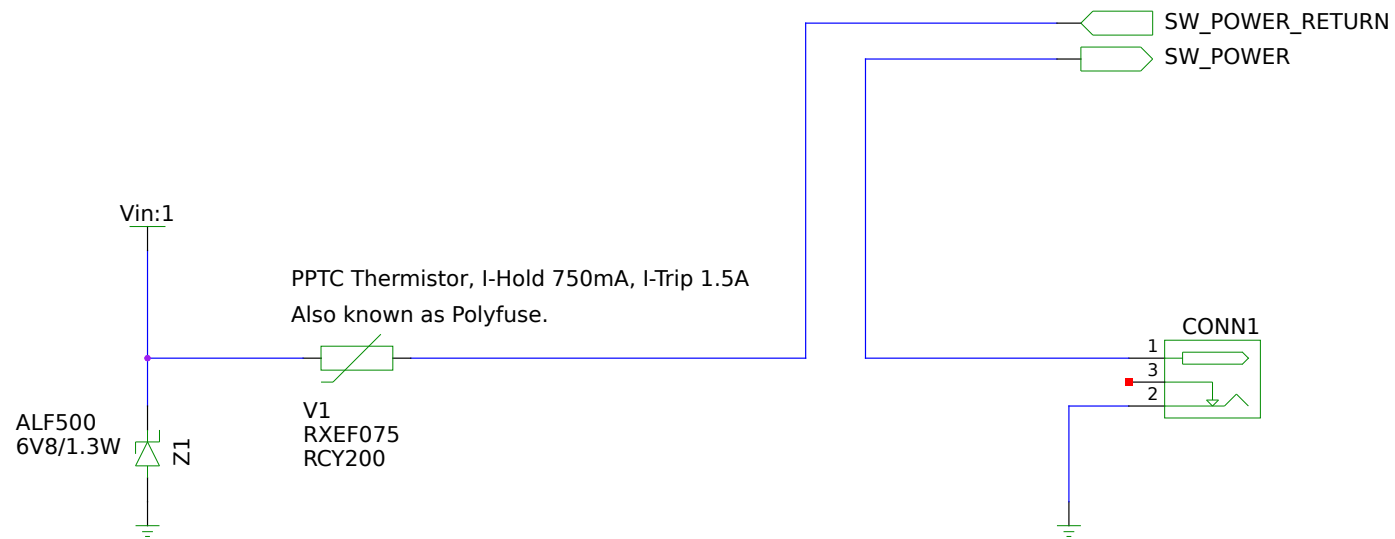
PAGE OF

Zerocat Dev Kit

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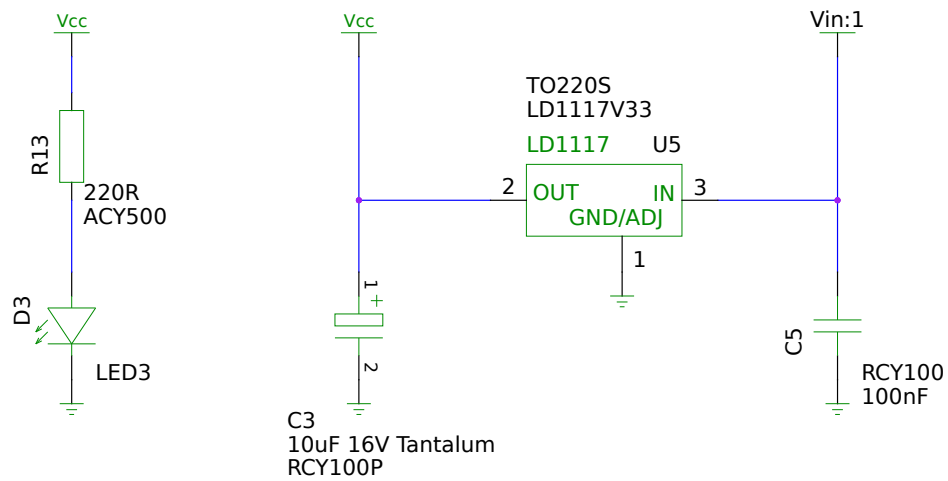


Power Input

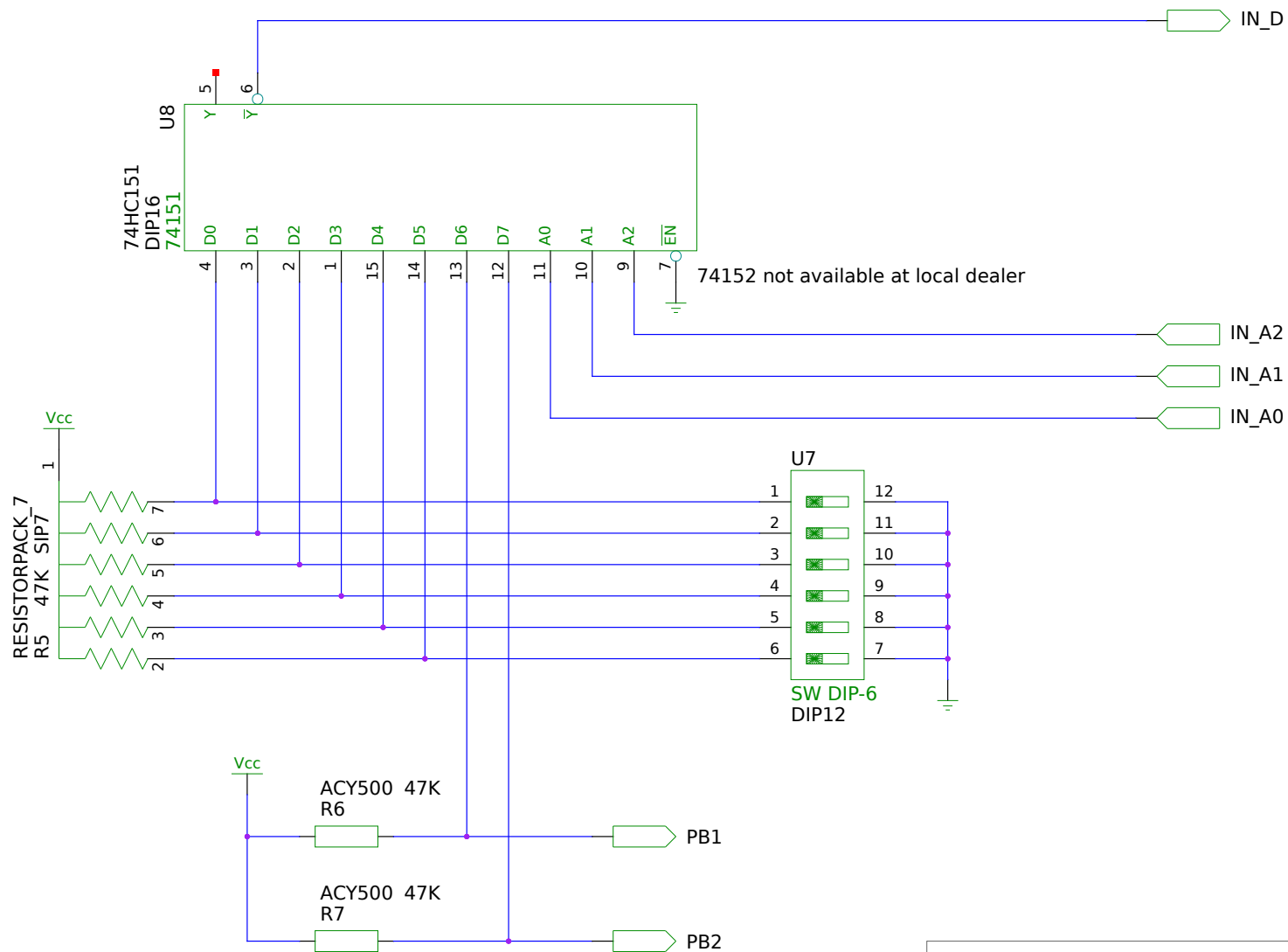
Zerocat Dev Kit

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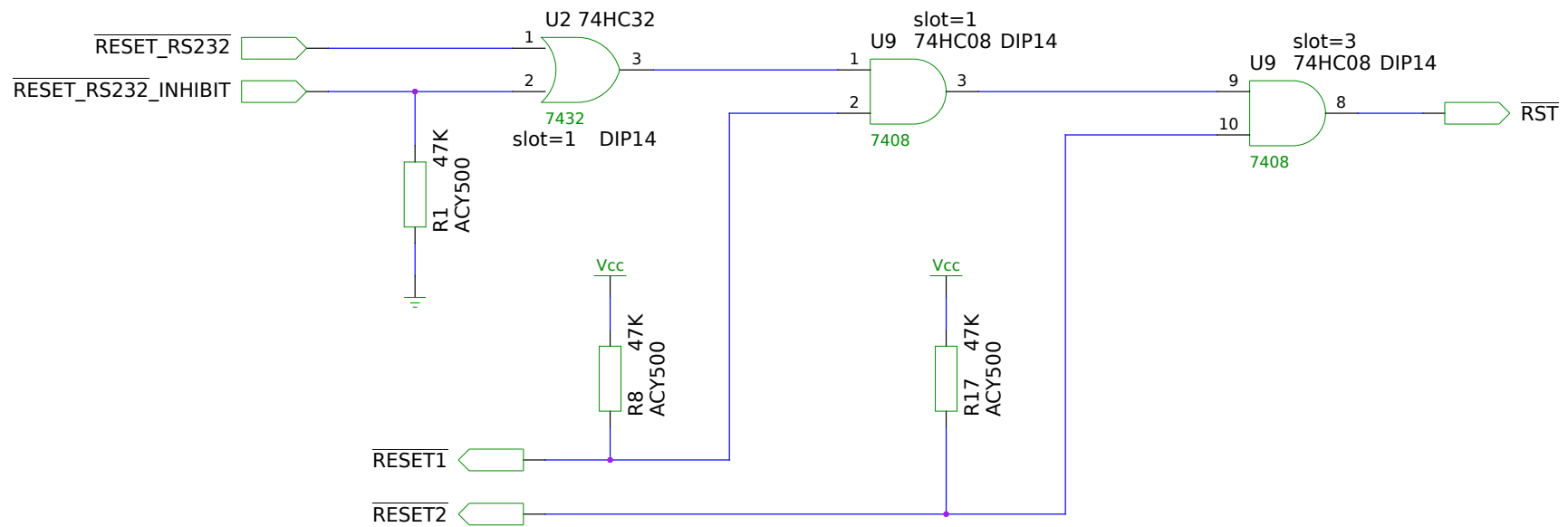
FILE:	multiboard-page06.sch	REVISION:	2117b7b4b (master)
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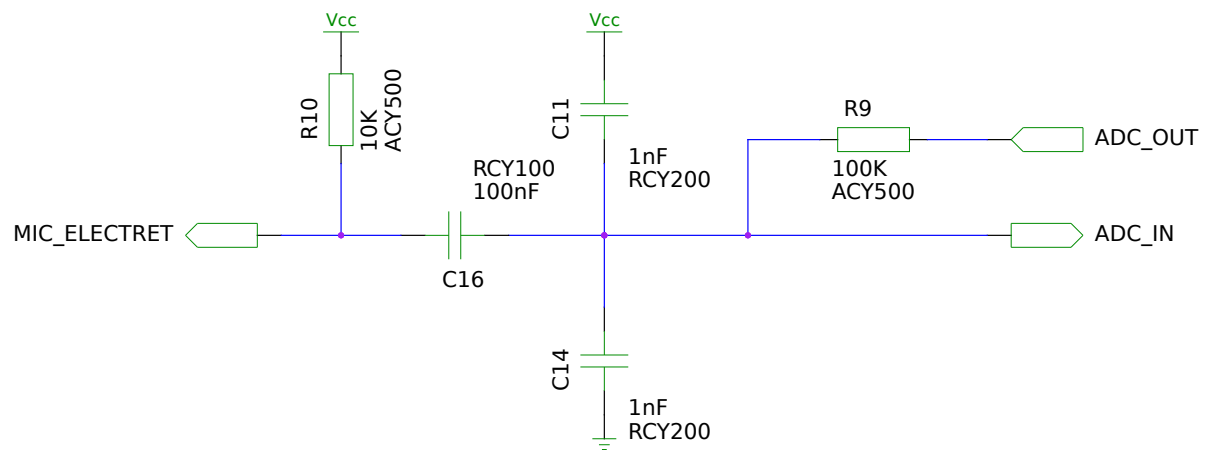
Voltage Regulator		Zerocat Dev Kit Put the fun back into hardware development! git://zerocat.org/zerocat/projects/zerocat-dev-kit.git	
TITLE			
FILE:	multiboard-page07.sch	REVISION:	2117b7b4b (master)
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Input Switches			Zerocat Dev Kit	
TITLE			Put the fun back into hardware development!	
FILE:			git://zerocat.org/zerocat/projects/zerocat-dev-kit.git	
PAGE			REVISION:	
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Reset Circuit		Zerocat Dev Kit	
TITLE		Put the fun back into hardware development! git://zerocat.org/zerocat/projects/zerocat-dev-kit.git	
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compare to Parallax AN008-SigmaDeltaADC-v1.0.pdf

ADC (AC in) Circuit

Zerocat Dev Kit

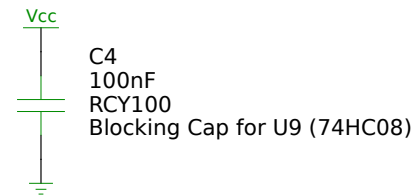
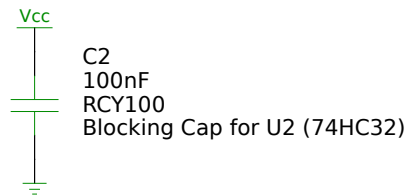
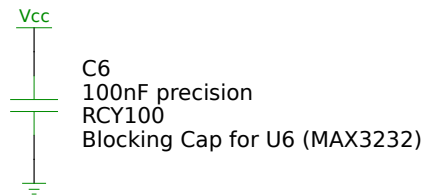
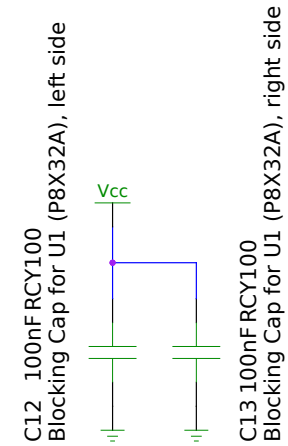
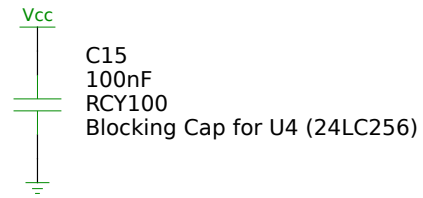
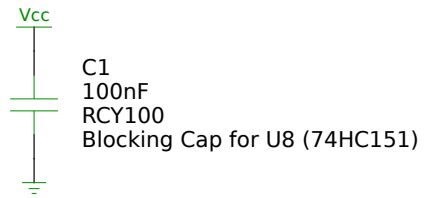
Put the fun back into hardware development!
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FILE: multiboard-page10.sch

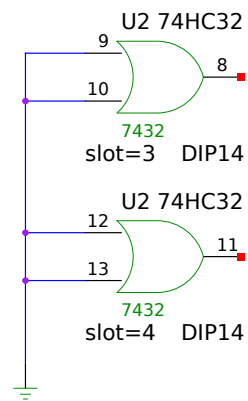
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Blocking Capacitors			Zerocat Dev Kit	
TITLE			Put the fun back into hardware development! git://zerocat.org/zerocat/projects/zerocat-dev-kit.git	
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Spare ICs, unused slots, etc.

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FILE: multiboard-page12.sch

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